

VEDANG INSTITUTE OF TECHNOLOGY

DIPLOMA DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING 1ST SEMESTER

DAY	09:00-09:50	09:50-10:40	10:40-11:30	11:30-12:20	12:20-01:30	01:30-02:20	02:20-03:10	03:10-04:00
MON	BEE	W/S (G2) / SCA (G1)			L	EP	CA LAB (G1) / EP LAB (G2)	
TUE	CA	EP	BEE	BEE	U	MATH I	CA LAB (G2) / LIBRARY (G1)	
WED	MATH I	W/S (G2) / SEMINAR (G1)			N	CA	EP LAB (G1) / LIBRARY (G2)	
THUR	BEE	W/S (G1) / SCA (G2)			C	MATH I	EP LAB (G2) / LIBRARY (G1)	
FRI	MATH I	EP	CA	CA	H	SEMINAR	CA LAB (G1) / LIBRARY (G2)	
SAT	MATH I	W/S (G1) / SEMINAR (G2)				EP	CA LAB (G2) / EP LAB (G1)	

THEORY

1. Computer Application
2. Engineering Physics
3. Engineering Mathematics-I
4. Basic Electrical & Electronics Engg.

LAB

5. Computer application Lab
6. Engg. Physics Lab
7. Workshop Practice
8. SCA

NAME OF THE FACULTY

- APARNA SAMANTARAY
 LINA MAHANTA
 SUSHREE SANGEETA ROU
 SUMITRA BEHERA
- APARNA SAMANTARAY
 LINA MAHANTA
 AMRUTA PANDA
 RADHA KUMARI PANI


 Prof. In-Charge
 Time Table

Principal
 Vedang Institute of Technology

VEDANG INSTITUTE OF TECHNOLOGY
 KHURDA


 29/07/2023

VEDANG INSTITUTE OF TECHNOLOGY

DIPLOMA DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING - 3RD SEMESTER

DAY	09:00-09:50	09:50-10:40	10:40-11:30	11:30-12:20	12:20-01:30	01:30-02:20	02:20-03:10	03:10-04:00
MON	MW(G-2)/SCAG-1)		EEM	EEM	L	ME LAB(G-1)/CS LAB(G-2)		
TUE	EME	CNT	ME LAB(G-1)/CS LAB(G-2)	EME	U	MW(G-2)/SCAG-1)		
WED	EMI	EM-III	ES	E-LIBRARY	N	ES	EM-III	CNT
THUR	MW(G-1)/SCAG-2)		EEM	EM-III	C	ME LAB(G-2)/CS LAB(G-1)		
FRI	EM-III	EME	ME LAB(G-2)/CS LAB(G-1)		H	MW(G-1)/SCAG-2)		ES
SAT	SCA	EME	EEM	LIBRARY		EM-III	CNT	ES

THEORY

1. Engg Mathematics-II(EM-II)
2. Circuit and Network Theory(CNT)
3. Electrical Engg Material (EM)
4. Elements of Mechanical Engg (EME)
5. Environmental studies (ES)

NAME OF THE FACULTY

1. SUSHREE SANGEETA ROU/T
 2. SUBASH CHANDRA BEHERA
 3. SAKSHI KUMAR PANDA
 4. SAKSHI KUMAR PANDA
 5. SUBASHREE SAHOO

LAB

6. Mechanical Engg Lab (ME LAB)
7. Circuit & Simulation Lab (CS LAB)
8. Mechanical Workshop (MW)
9. SCA

Prof. In-charge
Timed Table

Prof. 29/07/2023

Principal
Vedang Institute of Technology
VEDANG INSTITUTE OF TECHNOLOGY
KHURDA

DIPLOMA DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING - 5TH SEMESTER

[illegible]

NAME OF THE FACILITY

- | | |
|--|-----------------------|
| 1. Entrepreneurship and Management & Smart Technology (EMASTD) | SUBHASHREE SAHOO |
| 2. Digital Electronics & Microprocessor (DE&M) | S SANJAY KUMAR PATRA |
| 3. Analog & Digital Communication (ADC) | S/MITRA BEHERA |
| 4. Wave Propagation & Broadband Communication Engineering (WP&BCE) | SUBASH CHANDRA BEHERA |
| 5. Power Electronics & PLC (PE&PLC) | S SANJAY KUMAR PATRA |
| LAB | |
| 6. Power Electronics & PLC Lab (PE&PLC LAB) | S SANJAY KUMAR PATRA |
| 7. Digital Electronics & Microprocessor Lab (DE&M LAB) | S SANJAY KUMAR PATRA |
| 8. Analog & Digital Communication Lab(ADC LAB) | S/MITRA BEHERA |
| 9. Wave Propagation & Communication Engineering Lab (WP&BCE LAB) | SUBASH CHANDRA BEHERA |
| 10. Project Phase -1 | SANMUTAPRAGYA PANDA |
| 11. SCA | RADHA KUMARI PANI |

**Profit-to-Charge
Time-Ratio**

Vedang Institute of Technology

VEDANG INSTITUTE OF TECHNOLOGY
KHURDA

Principal
Frieda
29/02/2023