

VEDANG INSTITUTE OF TECHNOLOGY

DIPLOMA DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING - 2ND SEMESTER

DAY	09:00-09:50	09:50-10:40	10:40-11:30	11:30-12:20	12:20-01:30	01:30-02:20	02:20-03:10	03:10-04:00
MON	EC		ED (G2) / SCA (G1)		L	EM	ENG LAB (G1) / EC LAB (G2)	
TUE	MATH I	EM	EC	EC	U	ENG	ENG LAB (G2) / LIBRARY (G1)	
WED	MATH I		ED (G2) / SEMINAR (G1)		N	ENG	EC LAB (G1) / LIBRARY (G2)	
THUR	EC		ED (G1) / SCA (G2)		C	MATH I	EC LAB (G2) / LIBRARY (G1)	
FRI	MATH I	EM	ENG	ENG	H	SEMINAR	ENG LAB (G1) / LIBRARY (G2)	
SAT	MATH I		ED (G1) / SEMINAR (G2)			EM	ENG LAB (G2) / EC LAB (G1)	

THEORY

1. Communicative English (ENG)
2. Engineering Chemistry (EC)
3. Engineering Mathematics-1
4. Engg. Mechanics (EM)

NAME OF THE FACULTY

JYOTIRMAAYEE MISHRA
RADHA KUMARI PANI
SUSHREE SANGEETA ROUT
SUBHASHREE SAHOO

LAB

5. Comm. English Lab (ENG LAB)
6. Engg. Chemistry Lab (EC LAB)
7. Engineering Drawing (ED)

JYOTIRMAAYEE MISHRA
RADHA KUMARI PANI
PRIYANKA BARIK

Prof. in-Charge
Time Table

Vedang Institute of Technology

VEDANG INSTITUTE OF TECHNOLOGY
KHURDA

Prof. in-Charge
13/01/2024

VEDANG INSTITUTE OF TECHNOLOGY

DIPLOMA DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING - 4TH SEMESTER

DAY	09:00-09:50	09:50-10:40	10:40-11:30	11:30-12:20	12:20-01:30	01:30-02:20	02:20-03:10	03:10-04:00
MON	EM	EM LAB(G1)/MAT LAB(G2)			L	AE LAB(G1)/SCA(G2)		
TUE	AE&OP	EMI	EM	GTD	U	EM	EMI	AE&OP
WED	GTD	EM LAB(G1)/MAT LAB(G2)			N	AE LAB(G1)/LIBRARY(G2)		
THUR	GTD	GTD	AE&OP	EM	C	EM LAB(G2)/MAT LAB(G1)		
FRI	EMI	AE LAB(G1)/SCA(G2)			H	AE&OP	SEMINAR	
SAT	EMI	AE LAB(G2)/LIBRARY(G1)						

THEORY

1. Electrical Machine (EM)
2. Analog Electronics & OP(AE&OP)
3. Electrical Measurement & Instrumentation(EMI)
4. Generation, Transmission & Distribution (GTD)

LAB

5. Electrical Machine Lab (EM LAB)
6. Analog Electronics Lab (AE LAB)
7. Simulation Practice on MATLAB (MAT LAB)
- 8.SCA

NAME OF THE FACULTY

- SUMITRA BEHERA
S SANJAY KUMAR PATRA
SUBASH CHANDRA BEHERA
SMRUTIREKHA PANDA
SUMITRA BEHERA
S SANJAY KUMAR PATRA
SMRUTIREKHA PANDA
JYOTIRMAYEE MISHRA

Prof./In-Charge
Time Table

Vedang Institute of Technology
VEDANG INSTITUTE OF TECHNOLOGY
KHURDA

Principal
13/01/2024

VEDANG INSTITUTE OF TECHNOLOGY

DIPLOMA DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING - 6TH SEMESTER

DAY	09:00-09:50	09:50-10:40	10:40-11:30	11:30-12:20	12:20-01:30	01:30-02:20	02:20-03:10	03:10-04:00
MON	RES	E LIBRARY(G1)/LIBRARY(G2)	ACE LAB(G2)/LIBRARY(G1)	DSP	L	PROJECT II (G1) / MAT LAB (G2)	ACE LAB(G1)/SCA(G2)	
TUE	SCPD	ACE	ACE	SCPD	U	ACE	PROJECT II (G2) / MAT LAB (G1)	
WED	RES	SCPD	ACE LAB(G2)/SCA(G1)	SCPD	N	SCPD	PROJECT II (G2) / MAT LAB (G1)	
THUR	DSP	SEMINAR	PROJECT II (G1) / MAT LAB (G2)		C	DSP	ACE LAB(G1)/LIBRARY(G2)	
FRI	ACE	RES	SCA(G2)		H	DSP	SCPD	SEMINAR
SAT	ACE							

THEORY

1. Advance Communication Engineering (ACE)
2. Semiconductor & Protective Devices (SCPD)
3. Digital Signal Processing (DSP)
4. Renewable Energy Source(RES)

LAB

5. Advance Communication Engineering Lab(ACE LAB)
6. MAT LAB & Processing Simulation Lab (MAT LAB-I)
7. Project Phase-II
8. SCA

NAME OF FACULTY

- S SANJAY KUMAR PATRA
SMBUTREKHA PANDA
SUBASH CHANDRA BEHERA
SUBASHREE SAHOO

- S SANJAY KUMAR PATRA
S MITRA BEHERA
SUBASHCHANDRA BEHERA
JYOTIRMAYEE MISHRA

Principal
White Table

Principal
Vedang Institute of Technology

VEDANG INSTITUTE OF TECHNOLOGY
KHURDA

Principal
13/01/2024